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## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Govind MALALUR

FILING DATE

June 29, 2000

GROUP

2661

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<i>MAN</i>	AA	5,555,398	Sep. 10, 1996	Raman	711	243	Apr. 15, 1994
<i>MAN</i>	AB	5,644,784	Jul. 1, 1997	Peek	710	243	Mar. 3, 1995
<i>MAN</i>	AD	5,828,653	Oct. 27, 1998	Goss	370	240	Apr. 26, 1996
<i>MAN</i>	AE	5,423,015	Jun. 6, 1995	Chung	711	243	May 21, 1991
<i>MAN</i>	AF	5,696,899	Dec. 9, 1997	Kalwitz	709	228	Nov. 18, 1992
<i>MAN</i>	AG	6,061,351	May 9, 2000	Erimli et al.	370	390	Dec. 18, 1997
<i>MAN</i>	AH	5,748,631	May 5, 1998	Bergantino et al.	370	398	May 9, 1996
<i>MAN</i>	AI	6,119,196	Sep. 12, 2000	Muller et al.	710	243	Jun. 30, 1997
<i>MAN</i>	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.	711	159	Dec. 18, 1997
<i>MAN</i>	AK	5,473,607	Dec. 5, 1995	Hausman et al.	370	392	Aug. 9, 1993
<i>MAN</i>	AL	5,787,084	Jul. 28, 1998	Hoang et al.	370	390	Jun. 5, 1996
<i>MAN</i>	AM	5,845,081	Dec. 1, 1998	Rangarajan et al.	709	224	Sep. 3, 1996
<i>MAN</i>	AN	5,892,922	Apr. 6, 1999	Lorenz	709	238	Feb. 28, 1997
<i>MAN</i>	AO	5,802,287	Sep. 1, 1998	Rostoker et al.	370	395.5	Aug. 3, 1995
<i>MAN</i>	AP	6,011,795	Jan. 4, 2000	Varghese et al.	370	392	Mar. 20, 1997
<i>MAN</i>	AQ	6,185,185 B1	Feb. 6, 2001	Bass et al.	370	230	Nov. 21, 1997
<i>MAN</i>	AR	5,524,254	Jun. 4, 1996	Morgan et al.	309	245	Jul. 1, 1994
<i>MAN</i>	AS	5,987,507	Nov. 16, 1999	Creedon et al.	709	215	Dec. 29, 1998
<i>MAN</i>	AT	5,831,980	Nov. 3, 1998	Varma et al.	370	395.72	Sep. 13, 1996
<i>MAN</i>	AU	5,781,549	Jul. 14, 1998	Dai	370	398	Feb. 23, 1996
<i>MAN</i>	AV	6,041,053	Mar. 21, 2000	Douceur et al.	370	398	Sep. 18, 1997
<i>MAN</i>	AW	5,459,717	Oct. 17, 1995	Mullan et al.	370	351	Mar. 25, 1994
<i>MAN</i>	AX	5,909,686	Jun. 1, 1999	Muller et al.	707	104.1	Jun. 30, 1997
<i>MAN</i>	AY	5,887,187	Mar. 23, 1999	Rostoker et al.	712	29	Apr. 14, 1997
<i>MAN</i>	AZ	5,568,477	Oct. 22, 1996	Galand et al.	370	229	Jun. 27, 1995
<i>MAN</i>	BA	5,414,704	May 9, 1995	Spinney	370	389	Apr. 5, 1994
<i>MAN</i>	BB	5,390,173	Feb. 14, 1995	Spinney et al.	370	393	Oct. 22, 1992
<i>MAN</i>	BC	5,825,772	Oct. 20, 1998	Dobbins et al.	370	396	Apr. 2, 1996
<i>MAN</i>	BD	5,790,539	Aug. 4, 1998	Chao et al.	370	390	Jan. 29, 1996



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BA	BE	5,918,074	Jun 29, 1999	Wright et al.	710	52	Jul. 25, 1997
BA	BF	5,898,687	Apr. 27, 1999	Harriman et al.	370	390	Jul. 24, 1996
BA	BG	5,940,596	Aug. 17, 1999	Rajan et al.	709	242	Aug. 4, 1997
BA	BH	5,802,052	Sep. 1, 1998	Venkataraman	350	395	Jun. 26, 1996
BA	BI	5,842,038	Nov. 24, 1998	Williams et al.	710	75	Oct. 10, 1996
BA	BJ	5,742,613	Apr. 21, 1998	MacDonald	718	791	Apr. 27, 1993
BA	BK	5,579,301	Nov. 26, 1996	Ganson et al.	370	229	Feb. 28, 1994
BA	BL	5,278,789	Jan. 11, 1994	Inoue et al.	365	189	Dec. 6, 1991
BA	BM	5,652,579	Jul. 29, 1997	Yamada et al.	341	23	Aug. 15, 1996
BA	BN	5,499,295	Mar. 12, 1996	Cooper	380	270	Aug. 31, 1993

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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO PART.		
BA	BO	WO9900936A1	Jun. 12, 2001	WIPO					
BA	BP	EP0465090A1	Jan. 8, 1992	EPO					
BA	BQ	EP0859492A2	Aug. 19, 1998	EPO					
BA	BR	EP0312917A2	Apr. 26, 1989	EPO					
BA	BS	EP0853441A2	Jul. 15, 1998	EPO					
BA	BT	4-189023	Jul. 7, 1992	Japan					Abs
BA	BU	WO 98/09473	Mar. 5, 1998	WIPO					
BA	BV	EP0752796A2	Jan. 8, 1997	EPO					
BA	BW	EP0862349A2	Sep. 2, 1998	EPO					Abs
BA	BX	FR 2 725 573 - A1	Apr. 12, 1996	France					Abs
BA	BY	EP0854606A2	Jul. 22, 1998	EPO					
BA	BZ	WO 99/00948	Jan. 7, 1999	WIPO					
BA	CA	WO 99/00949	Jan. 7, 1999	WIPO					
BA	CB	WO 99/00950	Jan. 7, 1999	WIPO					
BA	CC	WO 99/00939	Jan. 7, 1999	WIPO					
BA	CD	WO 99/00938	Jan. 7, 1999	WIPO					
BA	CE	WO 99/00944	Jan. 7, 1999	WIPO					
BA	CF	WO 99/00945	Jan. 7, 1999	WIPO					
BA	CG	EP0849917A2	Jun. 24, 1998	EPO					
BA	CH	EP0907300A2	Apr. 7, 1999	EPO					

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BN	CI	"A High-Speed CMOS Circuit for 1.2-Gb/s 16 x 16 ATM Switching," Alain Chemarin et al. 8107 IEEE Journal of Solid-State Circuits 27(1992) July, No. 7, New York, US, pages 1116-1120
KN	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
CK	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
CL	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
CM	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
CN	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER

DATE CONSIDERED

\*EXAMINER:

Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.